

SIEMENS



Engineered with TIA Portal

SIMATIC S7-1200

It's the interplay that makes the difference

siemens.com/s7-1200



บริษัท เจพีโปรเกรส เอ็นจิเนียริ่ง จำกัด
JP PROGRESS ENGINEERING CO.,LTD.

35/36 M.2 T.Klongsam A.Klongluang Pathumthani 12120
35/36 หมู่.2 ตำบลคลองสาม อำเภอคลองหลวง จังหวัดปทุมธานี 12120
Tel : 02-832-8262 , 02-832-7253 Fax : 02-832-3590
www.jpprogress.com
E-mail : jppe_sales@hotmail.com , plc@jpprogress.com , info@jpprogress.com



Innovations – with firmware V2.0 & V3.0

SIMATIC S7-1200 (V2.0)

- ① GPRS
 - Machine-to-machine control schemes over cellular network
 - Easy implementation of remote machine data acquisition
 - Fault annunciation via SMS

Teleservice

- Centralized, remote configuration, programming and diagnostics
- Remote process code updates
- Modem, RS-232, ISDN, GSM, GPRS, and Ethernet available

Telecontrol

- Easy remote monitoring and control through GPRS architecture
- Monitoring and control for up to 5,000 stations
- Local buffering of data

② AS-i Master

- Master acc. to AS-i Spec. 3.0
- Up to 62 AS-i Slaves or 992 I/Os
- AS-i Power24V-capable (separate DCM)

③ PROFIBUS Master and Slave

- DP Master for Distributed I/O and Drives architectures
- DP Slave for integration into existing plant network

④ PROFINET

- Higher Level System interface
- RT Master for I/O and Drives
- No module required

Webserver

- Display of process values and diagnostics on standard web page
- User-defined web page via HTML
- Only requires IP address

Data logging

- User-defined runtime data values as CSV file in continuous logs
- Easy export via web page

PID controller

- 3-Step instructions for valve's or actuator's positioning

SIMATIC S7-1200 (V3.0)

- S7-1215C; 3 Types: DC/DC/DC; AC/DC/RLY; DC/DC/RLY – 130 mm Width
- 2 PN Ports – for two connections to HMI, IO or programming
- Battery Board – one year internal clock time with V3 CPUs
- 100 KB working memory – larger program process area
- 4 MB load memory – larger data storage area
- 55 ns bit performance
- Firmware update via PN port with V3 CPUs
- 2 AI2 AO – integrated on CPU
- 6 HSC (High-speed counters) – 3 at 100 kHz and 3 at 30 kHz clock rate
- 4 PTO/PWMs – 100 kHz onboard outputs or 200 kHz with signal board
- Extended internal clock retention time – upwards of 20 days typical
- Extended CPU temp rating – for hotter and colder applications

